

EEC 581 Computer Architecture, Fall 2008

Date	Content	Reading Assignments	Assignment Due
Aug 26 T	Introduction	Ch. 1	
Aug 28 R	Introduction	Ch. 1	
Sep 2 T	Pipeline	Appendix A	
Sep 4 R	Pipeline	Appendix A	
Sep 9 T	Instruction Set	Appendix B	Homework #1
Sep 11 R	Memory hierarchy	Appendix C	Project #1
Sep 16 T	Memory hierarchy	Appendix C	
Sep 18 R	Dynamic scheduling	Ch. 2	
Sep 23 T	Dynamic scheduling	Ch. 2	
Sep 25 R	Dynamic scheduling	Ch. 2	Homework #2
Sep 30 T	Limits on instruction-level parallelism	Ch. 3	
Oct 2 R	Limits on instruction-level parallelism	Ch. 3	
Oct 7 T	Project presentation		Project #2
Oct 9 R	Project presentation		Homework #3
Oct 14 T	NO CLASS		
Oct 16 R	Midterm Exam		
Oct 21 T	Limits on instruction-level parallelism	Ch. 3	
Oct 23 R	Multiprocessors	Ch. 4	
Oct 28 T	Multiprocessors	Ch. 4	
Oct 30 R	Multiprocessors	Ch. 4	
Nov 4 T	Multiprocessors	Ch. 4	
Nov 6 R	Memory hierarchy	Ch. 5	Project #3
Nov 11 T	NO CLASS (Veteran's Day)		
Nov 13 R	Memory hierarchy	Ch. 5	Homework #4
Nov 18 T	Memory hierarchy	Ch. 5	
Nov 20 R	Storage systems	Ch. 6	
Nov 25 T	Storage systems	Ch. 6	
Nov 27 R	NO CLASS (Thanksgiving)		
Dec 2 T	Project presentation		Project #4
Dec 4 R	Project presentation		Homework #5
Dec 11 R	Final Exam (600-750PM)		