

EEC 581 Computer Architecture, Fall 2007

Date	Content	Reading Assignments	Assignment Due
Aug 28 T	Introduction	Ch. 1	
Aug 30 R	Introduction	Ch. 1	
Sep 4 T	Pipeline	Appendix A	
Sep 6 R	Pipeline	Appendix A	
Sep 11 T	Instruction Set	Appendix B	Homework #1
Sep 13 R	Memory hierarchy	Appendix C	Project #1
Sep 18 T	NO CLASS		
Sep 20 R	Memory hierarchy	Appendix C	
Sep 25 T	Dynamic scheduling	Ch. 2	
Sep 27 R	Dynamic scheduling	Ch. 2	Homework #2
Oct 2 T	Dynamic scheduling	Ch. 2	
Oct 4 R	Limits on instruction-level parallelism	Ch. 3	
Oct 9 T	Project presentation		Project #2
Oct 11 R	Project presentation		Homework #3
Oct 16 T	Review		
Oct 18 R	Midterm Exam		
Oct 23 T	Limits on instruction-level parallelism	Ch. 3	
Oct 25 R	Limits on instruction-level parallelism	Ch. 3	
Oct 30 T	Multiprocessors	Ch. 4	
Nov 1 R	Multiprocessors	Ch. 4	
Nov 6 T	Multiprocessors	Ch. 4	
Nov 8 R	Multiprocessors	Ch. 4	Project #3
Nov 13 T	Memory hierarchy	Ch. 5	
Nov 15 R	Memory hierarchy	Ch. 5	Homework #4
Nov 20 T	Memory hierarchy	Ch. 5	
Nov 22 R	NO CLASS (Thanksgiving)		
Nov 27 T	Storage systems	Ch. 6	
Nov 29 R	Storage systems	Ch. 6	
Dec 4 T	Project presentation		Project #4
Dec 6 R	Project presentation		Homework #5
Dec 11 T	NO CLASS (Finals week)		
Dec 13 R	Final Exam (600-750PM)		