Errata of

*Embedded SoPC Design with Nios II Processor and Verilog Examples*

(Last Updated 08/01/2012)

- p.377, Figure 15.19: wrong image was embedded: the correct one should be
• The “→” symbols of RT operations in the ASMD chart are missing in p.157, Figure 7.2(a), due to printer’s font problem; the correct diagram should be

![ASMD segment diagram]

• The “→” symbols of RT operations in the ASMD charts are also missing in the following figures:
  o Figure 7.3(a) in p.159.
  o Figure 7.4 in p.160.
  o Figure 7.6 in p.162.
  o Figure 7.7 in p.167.
  o Figure 7.8 in p.169.
  o Figure 7.11 in p.179.
  o Figure 17.2 in p.426.
  o Figure 17.5 in p.431.
  o Figure 21.1 in p.666.
  o Figure 22.4.1 in p.690.